

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

J1033 U.S. PTO
09/878556
06/11/01

APPLICANT: S. SUNDAR KUMAR IYER ET AL

FOR: METHOD OF FORMING VARIABLE OXIDE THICKNESSES ACROSS
SEMICONDUCTOR CHIPS

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56, 1.97 AND 1.98

The Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

In compliance with the duty to disclose, applicants submit herewith a copy of each publication of which those designated by 37 CFR § 1.56 are aware; the publications are listed on attached form PTO-1449. The present Information Disclosure Statement shall not be construed as a representation that a search has been made or that no other art than that identified on the attached form PTO-1449 exists.

Consideration of this Information Disclosure Statement is respectfully requested, since the Examiner may consider the information provided herein material to the patentability of the subject application as defined in 37 CFR § 1.56.


Since this Information Disclosure Statement is being filed within three months of the filing date of the subject application, no fee or certification under 37 CFR § 1.97 (e) is required.

However, in the event the Commissioner of Patents deems that any fee is required under 37 CFR §§ 1.16 and 1.17 in connection with this application, applicants' attorneys authorize that such fee be charged to Deposit Account No. 09-0458.

Respectfully submitted,

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DATE OF DEPOSIT: June 11, 2001

BY SIGNATURE & DATE: 
Karen Cinq-Mars